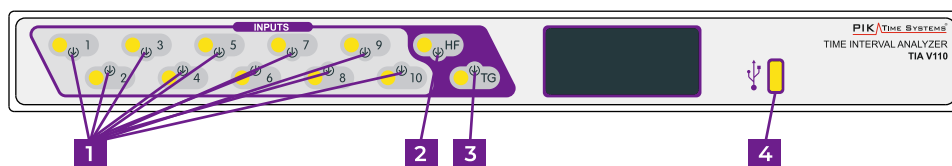


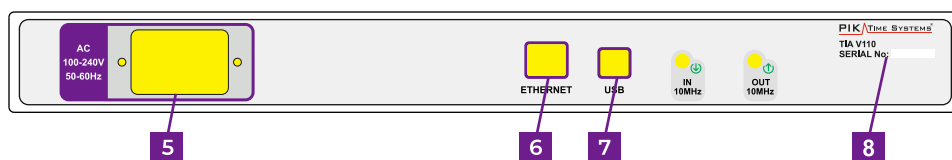
<ul style="list-style-type: none"> ■ Inputs IN1 - IN10 ■ Input TG ■ Input HF ■ Input 10 MHz (external reference clock) ■ Output 10 MHz ■ Built-in reference clock ■ Interfaces ■ Control ■ Power Supply ■ Size ■ Weight 	<p>Impedance: 50 Ω, DC coupled; SMA sockets Amplitude: ±5 V Active edge: selectable (rising or falling) Threshold: Selectable in range ±5 V</p> <p>Impedance: 1 kΩ Amplitude: TTL, threshold 1,4 V Active edge: selectable (rising or falling)</p> <p>Impedance: 50 Ω Input signal power: -10 dBm ÷ +10 dBm Input frequency: 200 MHz ÷ 6 GHz</p> <p>Impedance: 50 Ω Input signal power: -10 dBm ÷ +10 dBm Input frequency: 200 MHz ÷ 6 GHz</p> <p>Impedance: 50 Ω Input signal power: -10 dBm ÷ +10 dBm Input 10 MHz external signal copy: max. +10 dBm</p> <p>10 MHz, OCXO Stability 5×10^{-8} (-40° to +85° C) Aging 1×10^{-7}/year</p> <p>USB 2.0, typ A / Ethernet RJ-45 Text commands (description in the Programmer's Guide)</p> <p>230 V, 50 Hz, 100 W 395 (W) × 45 (H) × 330 (L) mm / Rack 19" 1U 6 kg</p>
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Front panel description



1. **Inputs 1 ÷ 10** – SMA sockets for connecting input signals for time relation evaluation.
2. **HF** – SMA socket for connecting high frequency input signals.
3. **TG** – SMA socket enabling connection of an optional trigger signal.
4. **USB** – USB communication connector.

Rear panel description



5. **230 V / 50 Hz** – AC power socket.
6. **Ethernet, USB** – Communication interface connectors.
7. **10 MHz In/Out** – Input and output of 10 MHz external reference clock signal.
8. **Serial No** – Individual analyzer identification number.