

Digital multiplexer/demultiplexer DMD V108

Introdution and key features

The DMD V108 Digital Multiplexer/Demultiplexer allows to connect an electrical signal from any selected input to any selected output(s). There are 8 inputs and 8 outputs at the user's disposal. The device is intended primarily for splitting time and frequency signals, in particular with standard values of 10 MHz, 5 MHz as well as 1PPS (Pulse Per Second), from reference clock sources with high stability.

The trigger threshold for input signals can be adjusted in the range from -5 V to +5 V. The device's advantages also include a low level of internal noise (timing jitter < 5 ps) and negligible differences in delays of signals distributed between any inputs and outputs (< 500 ps).



Specifications

	Number	of	inputs	8
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Spread of delays between	< 500 ps
different pairs of input – output	

■ **Inputs IN1** ÷ **IN8** Impedance: 50Ω , DC couped; SMA sockets

Amplitude: ±5 V

Threshold level: Selectable in a range $\pm 5 \text{ V}$

• Outputs OUT1 ÷ OUT8 Impedance: 50 k Ω , SMA sockets

Amplitude: $1,9 V_{pp}, t_r < 250 \text{ ps}, t_f < 200 \text{ ps}$

Interfaces USB 2.0, typ A

Ethernet RJ-45

Control Text commands (description in the Programmer's Guide)

■ **Power Supply** 230 V, 50 Hz, 50 W

■ **Size** 395 (W) × 45 (H) × 330 (L) mm / Rack 19" 1U

• Weight 6 kg

Front panel description



- **1. Inputs 1** \div **8** SMA sockets for connecting input signals.
- **2. Outputs 1 \div 8** SMA sockets outputting signals.
- **3. USB** USB communication connector.

Rear panel description



- **4. 230 V / 50 Hz** AC power socket.
- **5. Ethernet, USB** Communication interface connectors.
- **6. Serial No** Individual analyzer identification number.







